160 External Memory 9 140 150 Bus Bridge Master3 Slave3 114 Decoder 132 Master2 Slave2 Arbiter Host Processor (CPU) Master1 Slave1

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Fig. 2

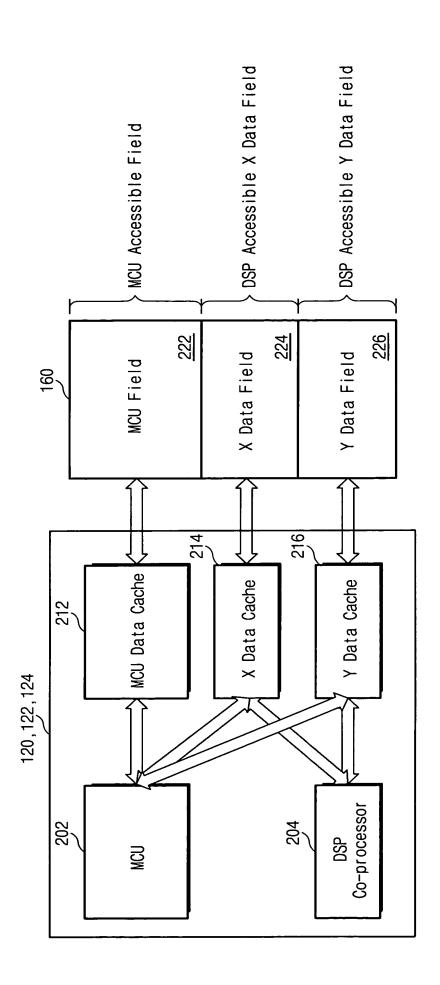


Fig. 3

